



Ultra-fast all-optical full-adder based on nonlinear photonic crystal resonant cavities

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Abstract

In this paper, a new photonic crystal-based full-adder for the summation of three bits has been proposed. For realizing this device, three input waveguides are connected to the main waveguide. An optical power splitter is placed at the end of this waveguide. Concerning the amount of optical intensity inside this waveguide, two nonlinear resonant cavities transmit the waves toward the correct ports. When the cavities do not drop the optical waves, the splitter guides them toward the output ports. The maximum delay time of the presented structure is around 0.5 ps and shows the fastest response among the reported works. This improvement is obtained due to using the resonant cavities. The time analysis results in a maximum working frequency of 2 THz. Also, designing the structure in $93 \mu\text{m}^2$ demonstrates that it is more compact than the previous works. The normalized low and high margins are obtained around 10% and 85%, respectively. So, the proposed device is capable of considering optical processing circuits.

Keywords Full-adder · Kerr effect · Photonic bandgap · Photonic crystal · Resonant cavity

1 Introduction

Due to increasing demands for wide bandwidth and ultra-fast processing, many attempts have been made to design and develop the optical processing systems. One of the essential components in digital circuits is the full-adder. This device sums three bits and generates two output bits, sum and carry. The main challenge in designing n -bit full-adders is the latency of developing the carry bit for the next stages. The electrical full-adders suffer the mentioned latency when the number of bits is increased. Different ideas such as generating carry signals in the form of the optical waves [1] and designing plasmonic-based [2] and photonic crystal-based [3–7] structures have been proposed to overcome this challenge.

Ying et al. [1] proposed an electro-optical modulator for generating the carry in optical waves. They used a continuous wave and modulated it as the optical carry for the next

stages. Although the electric signals modulate the carry, the output optical signal should be converted to the electric form. This issue has not been considered in their work. Concerning the reported time for the electro-optical modulators (50 ps), it seems that this idea will not be applicable to design the ultra-fast processors. Xie et al. [2] designed and fabricated the cross-connected plasmonic waveguides to utilize the all-optical full-adder. Two plasmonic nano-cavities were side-coupled with the output waveguides. These cavities were covered with high-nonlinear coefficient nano-composite layers. The plasmonic-based full-adder working states obtained correspond to the different modes, while the single-mode designing is preferred for operation. Although the time analysis was not reported, the all-optical processing can improve the time-consuming compared to the previous work [1].

Photonic crystals (PhCs) [8, 9] are known as the proper structures for designing all-optical devices. They are the periodic arrangement of dielectric materials that propagate and manipulate the light flow in the desired waveguides. The wavelength should be inside the photonic bandgap (PBG) of the structure to confine the waveguide's optical waves. Scalability, small size, and no need for electrical bias are the main advantages of PhC-based devices [10–14]. In recent years, several PhC-based devices such as optical

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filters [15–22], demultiplexers [23–29], logic gates [30–38], decoders [39–43], encoders [44–48], and analog-to-digital converters [49–54] have been proposed.

Alipour-Banaei and Seif-Dargahi [3] proposed a photonic crystal-based optical full-adder. The proposed structure was realized by cascading two optical half-adders. In this study, two nonlinear ring resonators with similar optical characteristics were used. Although they reported the rise time of 2 ps, the designed structure area was as large as 439 μm^2 . Cheraghi et al. proposed another PhC-based full-adder in which the dropping of optical waves occurred concerning the amount of optical intensity [4]. They used four nonlinear resonant cavities in a two-dimensional square lattice for guiding the waves toward the desired output ports [4]. Although the area was reduced to 396 μm^2 compared to the previous work [3], the rise time of 8 ps was obtained. Swarnakar et al. [5] designed a structure based on the cross-connected waveguides and used the inputs with different phases. They succeeded in reducing the rise time to 1 ps compared to the previous works. Moradi et al. [6] have designed a square arrangement of rods, including two ring resonators. They improved the difference of margins; however, the rise time was increased to 3 ps. Recently, Vali-Nasab et al. [7] have proposed a structure including two cross-connected waveguides for both output ports. They placed two nonlinear rods at the cross-connections to make the desired interferences. The small size around 72 μm^2 and the rise time of 2 ps are the main advantages of this structure.

Considering the structures mentioned above for optical full-adders demonstrates that designing the ultra-fast structures is an attractive challenge for researchers. In this paper, concerning the presented idea by Cheraghi et al. [4], we will propose an ultra-fast and compact structure for all-optical full-adder. The structure includes two nonlinear resonant cavities. Incoming optical signals from the inputs are combined and transmitted to the main waveguide. Concerning the amount of the optical intensity in the main waveguide, two resonant cavities guide the waves toward the desired output ports. Cheraghi et al. used two resonant cavities at the end of the main waveguide for dropping the waves to the state that all input bits were equal to 1 [4]. Although these cavities assisted in obtaining the correct operation, the reflected waves interfered with the traveling ones in the main waveguide for other states. This issue resulted in increasing the rise time and decreasing the margin of logic 1. In this study, a beam splitter has been placed at the end of the main waveguide to divide the optical signal into two equal parts. Each part activates one of the output ports when all input ports are at logic 1. The less portion of the reflected waves affects the traveling waves in the main waveguide for other states. For improving the wave dropping from the main waveguide to the cavities, the radii of the first rods at the cavities were tuned at 84 nm. Therefore, the proposed

structure's rise time was improved to 0.5 ps compared to all previous works.

Concerning the lattice constant of 475 nm and the radius of 105 nm, the structure's footprint was also improved. Besides, this work includes a more considerable difference between the mentioned margins than one for Refs [3–5, 7]. In Sect. 2, the design procedure of the proposed structure will be introduced. Then, the simulation results will be shown and compared to ones for other works, in Sect. 3. The conclusion of this work will be presented in Sect. 4.

2 Design procedure

A square lattice of dielectric rods with the air gaps, including 18 rows and 23 columns along the x and z directions, is used as the two-dimensional fundamental structure. The refractive index and radius of the dielectric rods are 3.1 and 0.22a, respectively, where $a=475$ nm is the lattice constant. According to Bragg's theory, this assumption helps obtain a large PBG for the proposed structure [55]. The plane wave expansion method was used to calculate the band diagram [56]. In Maxwell's equations, electric (E) and magnetic (H) fields could be described in the Fourier's series because of the periodic rods. So, $\left(\frac{\omega}{c}\right)^2$ as the eigenvalue of $\frac{1}{\epsilon_r} \nabla \times \nabla \times E = \left(\frac{\omega}{c}\right)^2 E$ and $\nabla \times \frac{1}{\epsilon_r} \nabla \times H = \left(\frac{\omega}{c}\right)^2 H$ equations was calculated for the different wave vectors [2]. ω is the frequency, c is the light velocity in the vacuum, and ϵ is the dielectric constant. The band diagram and the Brillouin zone of the PhC structure are shown in Fig. 1. One can see the presented lattice includes a PBG (blue hatched area) in TM mode. Concerning $a=475$ nm, this gap covers the range of 1131–1638 nm. The obtained wide range is advantageous to the structure and covers the optical communication bands such as C and L [57].

For realizing the proposed full-adder structure, three waveguides (W1, W2, and W3) are connected to three input ports A, B, and C, respectively (Fig. 2). The launched signals from A, B, and C ports are guided toward W5 waveguide through W1, W2, W3, and W4. Two resonant cavities (C1 and C2), including nonlinear rods, are located on both sides of W5 for making the dropping operation concerning the amount of optical intensity inside W5.

In this structure, two nonlinear rods (with green and blue colors) have been used in both cavities. The green rod is made of a doped glass with a linear refractive index and an optical Kerr coefficient of 1.4 and 10–14 m^2/W , respectively [53]. Another nonlinear rod is made of the chalcogenide with a linear refractive index of 3.1 and an optical Kerr coefficient of $9 \times 10^{-17} \text{ m}^2/\text{W}$ [58]. The radii of the rods are shown in Fig. 2.

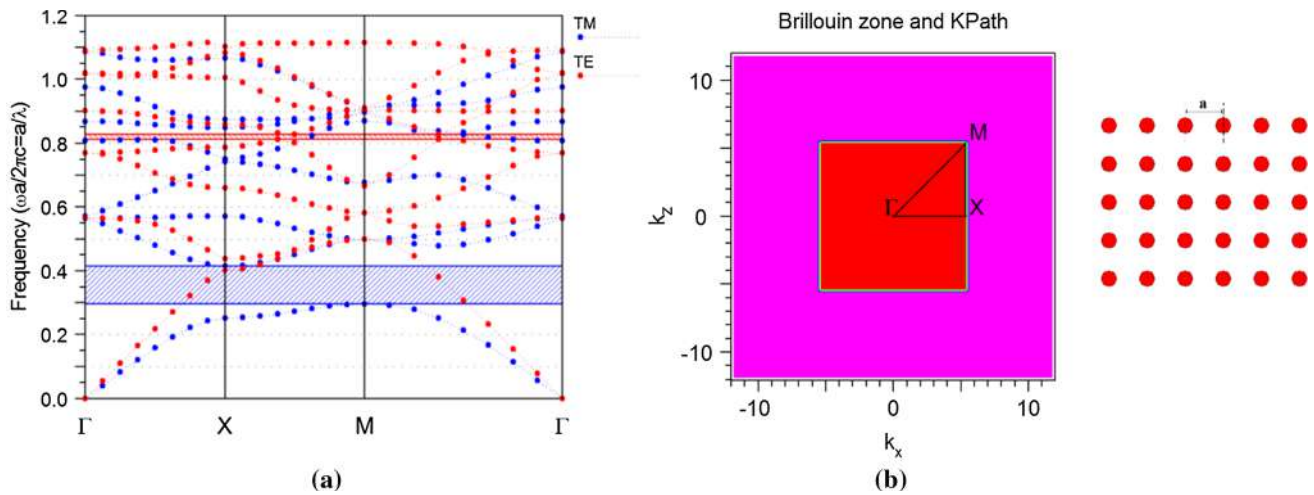


Fig. 1 **a** The calculated band diagram, **b** the Brillouin zone of the structure at x and z directions. The red disks present a top view of the rods (Color figure online)

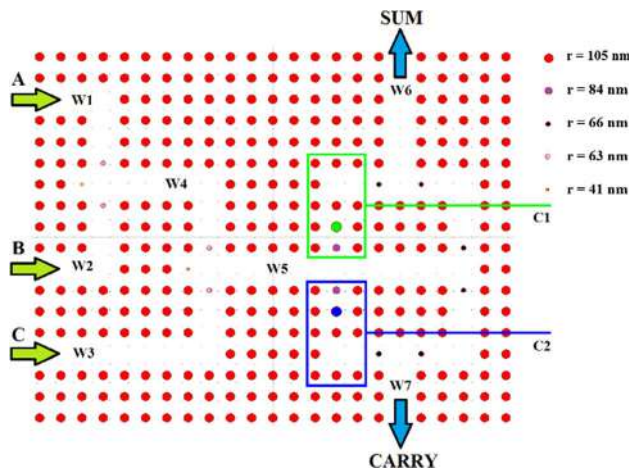


Fig. 2 The proposed structure for full-adder

The refractive index of nonlinear rods (n) is changed according to the incident optical intensity (I). This issue is known as the optical Kerr effect and is defined by $n = n_0 + kI$, where n_0 is the linear refractive index and k is the nonlinear coefficient [59, 60]. The resonant wavelength of both cavities depends on the refractive index of the nonlinear rods. Also, the amount of the optical intensity inside $W5$ is changed for the different states of inputs. By tuning the radii of nonlinear rods, the optical waves will be transmitted from $W5$ to $W6$ and $W7$ waveguides when the optical intensity is equal to I_0 and $2I_0$, respectively. I_0 shows the value of optical intensity inside $W5$ when one of the input ports is active. In this state, $C1$ cavity couples the waves to $W6$, so SUM port is activated.

When two input ports are at logic, the optical intensity inside $W5$ is equal to $2I_0$. So, the optical signals are coupled

Table 1 Working states of the structure

Input states			Light intensity inside $W5$	Logic for output ports	
A	B	C		SUM	CARRY
0	0	0	–	0	0
0	0	1	I_0	1	0
0	1	0	I_0	1	0
0	1	1	$2I_0$	0	1
1	0	0	I_0	1	0
1	0	1	$2I_0$	0	1
1	1	0	$2I_0$	0	1
1	1	1	$3I_0$	1	1

to $W7$ through $C2$ cavity, and $CARRY$ port is activated. If all input ports are at logic 1, the amount of the optical intensity inside $W5$ is equal to $3I_0$, so the waves reach the end of $W5$ waveguide. At the end of $W5$, a splitter has been placed to guide the optical waves toward SUM and $CARRY$ ports. In this state, both output ports are activated. The working states are summarized in Table 1.

Concerning the lattice constant and the radius of rods, the area is just around $93 \mu\text{m}^2$, so it is more compact than other structures [3–6].

3 Simulation and results

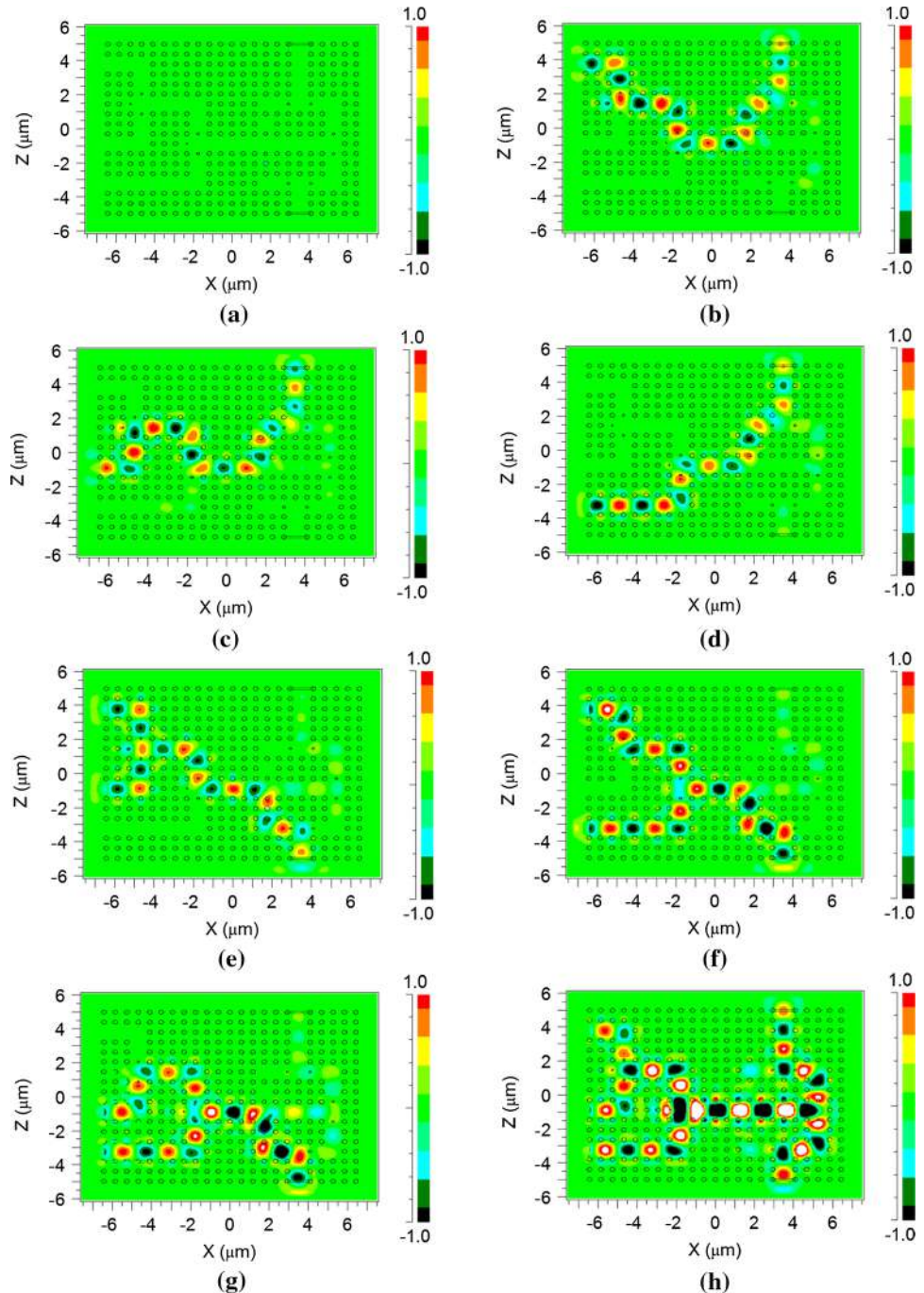
The finite difference time domain (FDTD) method is used to simulate the optical wave propagation throughout the structure. In this method, Maxwell’s equations are discretized in space and time. Based on Yee’s cell, the electric and magnetic fields at each cell are calculated using the four

neighboring cells [61]. The structure is discretized in space by $\Delta x = \Delta z = 10$ nm in which the length of the unit cell is smaller than $\lambda/10$ where λ is the wavelength. Three optical waves with a central wavelength of 1550 nm and an optical intensity of $100 \text{ mW}/\mu\text{m}^2$ are used for the input signals. According to the Courant condition, the time step of 10 is used for the time step [61].

Because the structure includes three input ports, eight different working states are possible that should be considered. Figure 3 shows the distribution of the normalized optical

fields inside the proposed structure. Furthermore, the pulse response has been calculated for all possible states, as shown in Fig. 4. The duration time of the pulse is 9 ps. Figure 4 shows that the normalized power at the output port is defined as the amount of optical power at the output port divided by the input power at one port. So, the normalized value can be more than one if two or three input ports are activated. In this study, the maximum amount of the rise and fall times is known as a delay. The rise time is defined as the time that the signal goes up to reach 90% of the steady-state value.

Fig. 3 Optical wave propagation inside the proposed structure for all working states; **a** $ABC=000$, **b** $ABC=100$, **c** $ABC=010$, **d** $ABC=001$, **e** $ABC=110$, **f** $ABC=101$, **g** $ABC=011$, **h** $ABC=111$



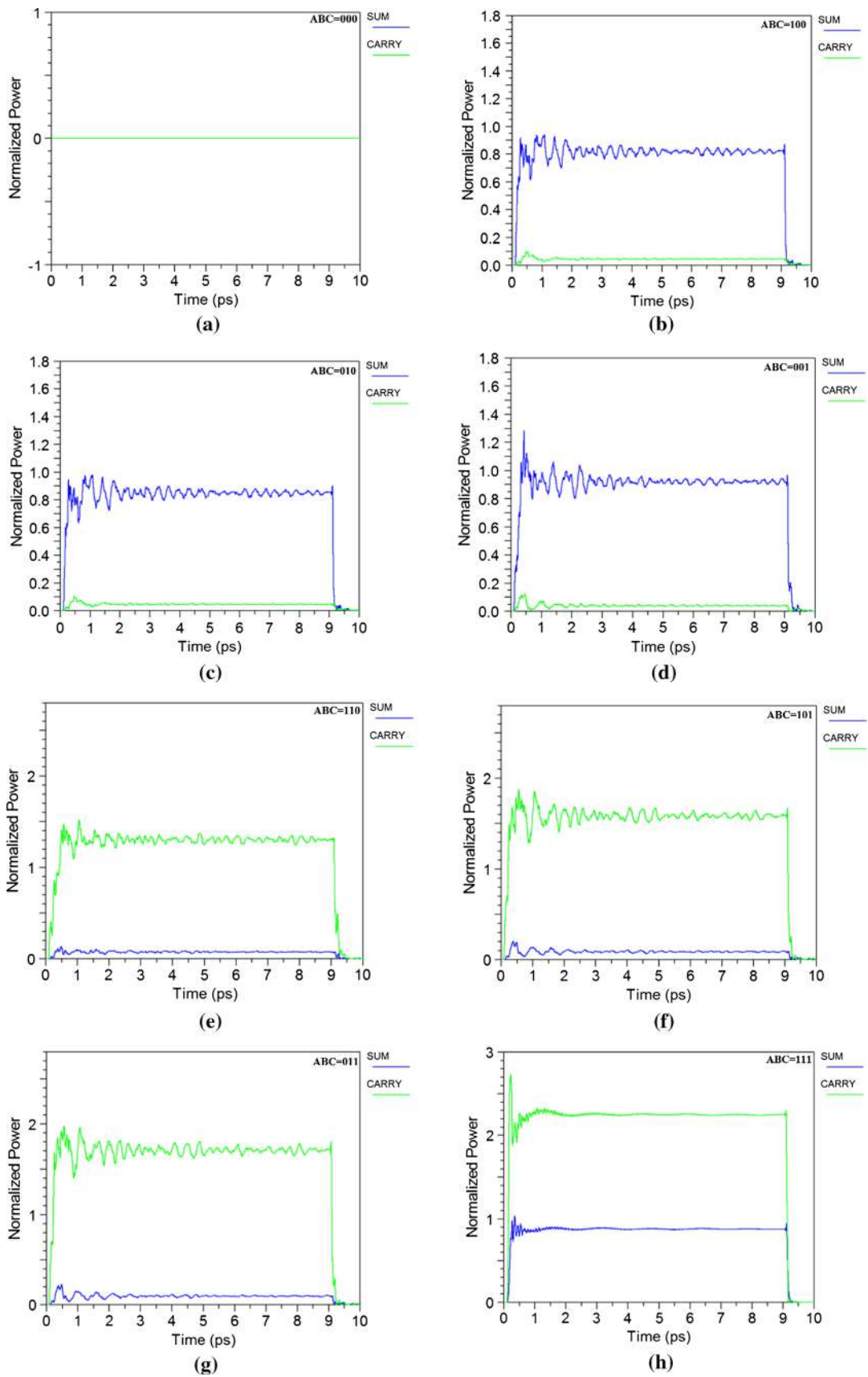


Fig. 4 Time response for different states; a $ABC = 000$, b $ABC = 100$, c $ABC = 010$, d $ABC = 001$, e $ABC = 110$, f $ABC = 101$, g $ABC = 011$, h $ABC = 111$

The fall time is also defined as the needed time to decrease 10% of the initial value.

It is evident that when the input ports are inactive, there is no optical wave inside the structure, so the output ports are assumed at logic 0 (Figs. 3a and 4a). As shown in Fig. 3b, c, and d, when the optical waves are applied to one of the input ports (i.e., $A = 1, B = C = 0$, or $B = 1, A = C = 0$, or $C = 1, A = B = 0$), C1 cavity drops them to W6 waveguide. Therefore, the minimum amount of the normalized optical intensities at SUM port and the maximum one at CARRY port are equal to 85% and 4%, respectively. As a result, SUM and CARRY ports are at logics 1 and 0, respectively. For these states, the maximum delay time is around 0.41 ps, and the time response diagrams are shown in Fig. 4b, c, and d.

When the optical waves are applied to two input ports (i.e., $A = B = 1, C = 0$ or $A = C = 1, B = 0$ or $B = C = 1, A = 0$), C2 cavity guides the waves to W7 waveguide and CARRY port is activated. Figure 3e, f, and g shows the optical wave propagation inside the structure when one input port is inactive. The time analysis of these states demonstrates the maximum amount of the normalized optical intensities at SUM port, and the minimum one at CARRY port are 10% and

134%, respectively (Fig. 4e, f, and g). Also, the maximum delay time of 0.46 ps is obtained for these states.

Finally, if the optical waves are applied to all input ports ($A = B = C = 1$), the cavities do not drop them to W6 and W7, so they reach the splitter and activate the output ports (Fig. 3h). In this state, the amount of the normalized optical intensities at SUM and CARRY ports are equal to 88% and 225%, respectively (Fig. 4h). So, the output ports are assumed at logic 1. The delay time of this state is around 0.5 ps.

Figure 5 shows the normalized power in the cavities versus the optical intensity inside W5 waveguide. It can be seen that the maximum droppings through C1 and C2 cavities occur for I0 and 2I0, respectively, where I0 is equal to $0.1 \text{ W}/\mu\text{m}^2$. According to Bragg's theory, the reflected waves from the periodic bilayers are in phase if the equation $n_1d_1 + n_2d_2 = \lambda/2$ is satisfied [62]. n_1 and n_2 are the refractive indices, and d_1 and d_2 are the thicknesses of the layers. So, using the different radii in cavities, the dropping operation for I0 and 2I0 is possible.

The performance of the proposed structure for all working states is summarized in Table 2. Comparing the calculated table to the truth table of a typical full-adder confirms the proposed structure's correct operation. Based on the obtained results, the minimum value of the normalized intensity at output ports for logic 1 is 85%. Also, the maximum amount of the intensity for logic 0 is 10%. These values are the worst cases to determine the logic margins of the structure. Therefore, 10% and 85% can be assumed as the margins of logics 0 and 1, respectively.

The contrast ratio is defined by $10 \times \log(M1/M0)$, where M1 and M0 are the margins for logics 1 and 0, respectively [7]. Concerning $M1 = 85\%$ and $M0 = 10\%$, the contrast ratio is equal to 9.3 dB. Bit rate refers to the speed at which the data are transferred and is defined by $1/T_{ss}$ where T_{ss} is the steady-state time. The steady-state time is defined as when the variation of the signal is confined to 5% of the final value. So, the bit rate of the structure is around 0.2 THz.

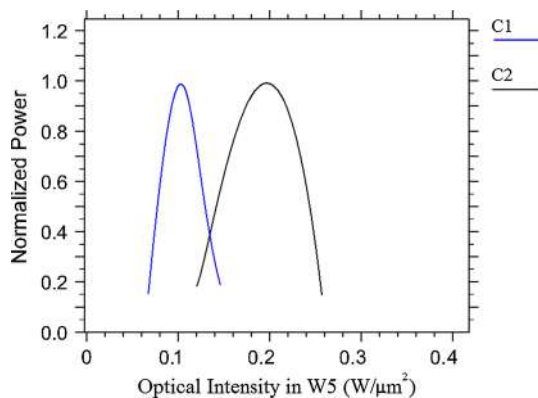


Fig. 5 Normalized power in the cavities versus the optical intensity in W5

Table 2 Simulation results for different input states

Input states			Output ports				Rise time (ps)	Fall time (ps)
A	B	C	Logic		Normalized intensity (%)			
A	B	C	SUM	CARRY	SUM	CARRY		
0	0	0	0	0	0	0	–	–
0	0	1	1	0	94	4	0.39	0.21
0	1	0	1	0	86	4	0.41	0.26
0	1	1	0	1	10	176	0.44	0.23
1	0	0	1	0	85	4	0.4	0.24
1	0	1	0	1	8	165	0.42	0.27
1	1	0	0	1	8	134	0.46	0.32
1	1	1	1	1	88	225	0.5	0.27

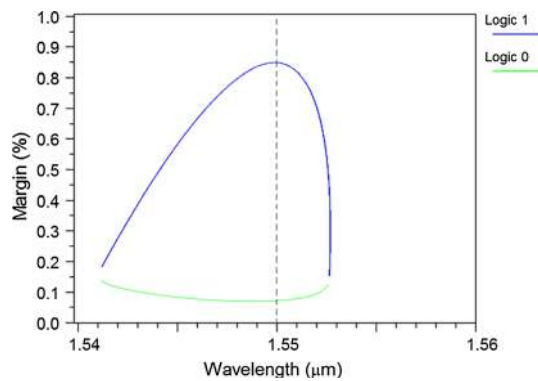


Fig. 6 The calculated margins of the structure for the different wavelengths

Table 3 Comparison of the obtained results with other works

Work	Area (μm^2)	T_d (ps)	DM (%)
[48]	439	2	55
[49]	396	8	43
[50]	346	1	28
[51]	560	3	81
[52]	72	2	60
This work	93	0.5	75

Figure 6 shows the margin of logics versus the wavelength. All possible states were considered for each wavelength, and the margins of logics 0 and 1 were calculated. It can be seen that the maximum contrast ratio is obtained at the wavelength of 1550 nm. Also, there is a gap between the margins for the range of 1541–1552 nm. This structure has been designed for the wavelength of 1550 nm, so for another wavelength, the radii of defect rods should be changed.

To additional assessment, a comparison of the proposed full-adder with other works is given in Table 3. Area, delay time (T_d), and the difference between margins for logics 0 and 1 (DM) as the essential parameters are investigated for this work and Refs [3–7]. The small size of the presented structure is advantageous for this work and is a proper result for optical integrated circuits. The compactness of the structure results in decreasing the delay time. One can see that the obtained delay time has been reduced to 0.5 ps compared to Refs [3–7]. Concerning the value of 0.5 ps, the maximum working frequency of 2 THz is obtained for the proposed device. Concerning the increasing demands for ultra-fast processing, this improvement can be considered an essential advantage in designing all-optical full-adder. The last column of Table 3 presents the difference in margins for logics 0 and 1. It can be seen that the calculated value is more than one reported in Refs [3–5, 7]. Consequently, the

presented all-optical full-adder can be potentially considered as a proper candidate for optical integrated circuits.

Many attempts have been done for the fabrication of photonic crystal-based structures [63–72]. They have used different methods to fabricate these structures, such as colloidal self-assembly, electron beam lithography, and direct writing via multiphoton microlithography. Deviation of the rods respect to the vertical axis, the ruggedness of rods, and the non-periodicity of the lattice are the main challenges of the fabrication process. Concerning these researches, they have been succeeded in decreasing the radius of rods to 40 nm. The smallest radius of rods in the presented structure is equal to 41 nm. So, one can be optimistic about fabricating the proposed device.

4 Conclusion

In this paper, a new photonic crystal-based structure for realizing an all-optical full-adder was proposed. Input signals were guided to the main waveguide through four waveguides. Two resonant cavities including nonlinear rods and one splitter transmitted optical waves toward the output ports. The simulation of the optical wave propagation for eight possible states demonstrated the correct operation of the structure. Small size and fast response of the presented device compared to other works were the main advantages of this work. The simulation results showed that the area and the delay time of the proposed full-adder were around $93 \mu\text{m}^2$ and 0.5 ps, respectively. Furthermore, the amount of 10% and 85% was obtained as the margins for logics 0 and 1, respectively. Based on the simulation results, the presented all-optical full-adder can be considered for optical integrated circuits.

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